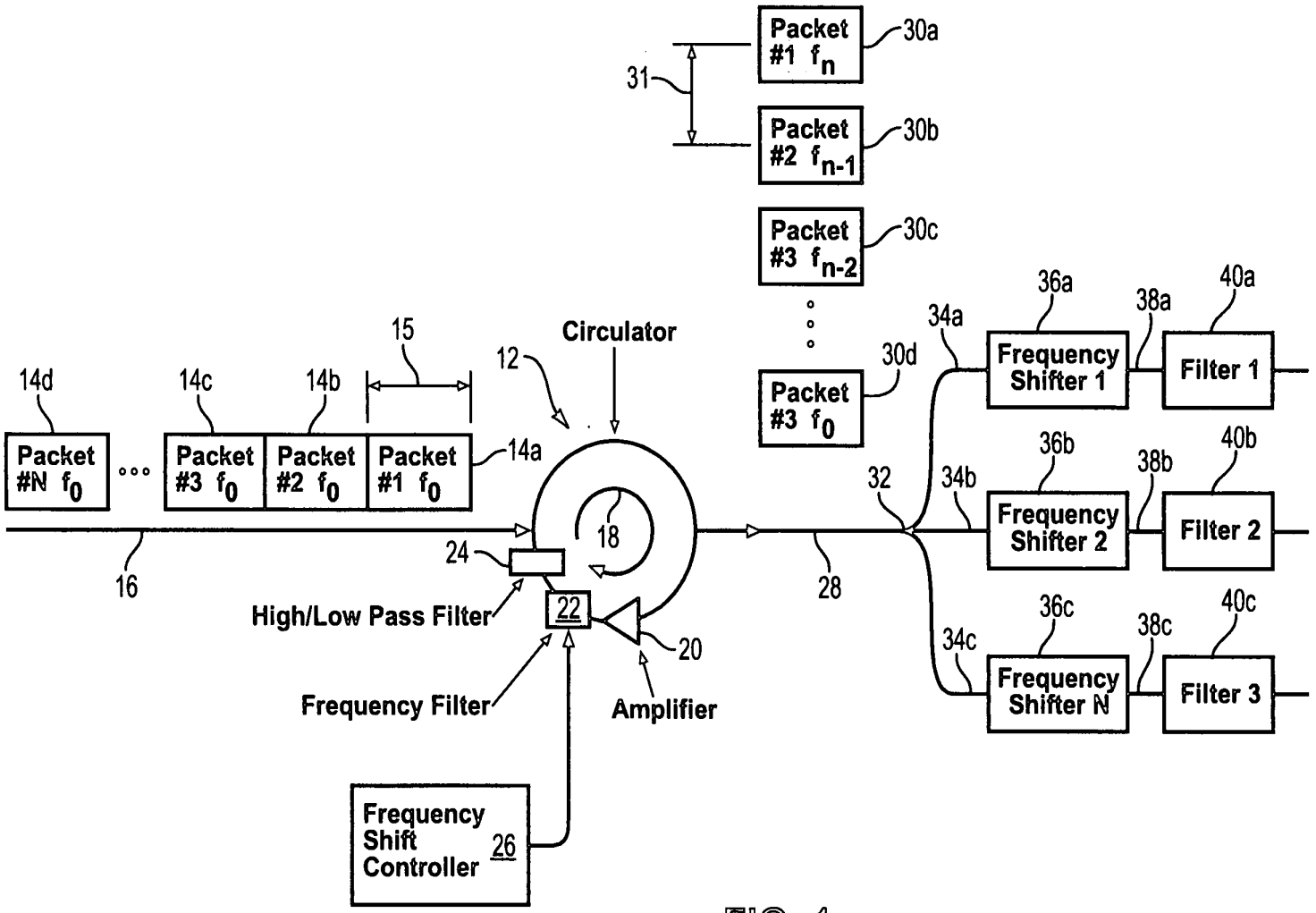

Packet #1 f_n
30a
31
Packet #2 f_{n-1}
30b
Packet #3 f_{n-2}
30c
Packet #3 f_0
30d
Circulator
12
15
14d
14c
14b
Packet #N f_0
Packet #3 f_0
Packet #2 f_0
Packet #1 f_0
14a
16
18
24
High/Low Pass Filter
22
Frequency Filter
20
Amplifier
26
Frequency Shift Controller
32
28
34a
34b
34c
36a
36b
36c
38a
38b
38c
40a
40b
40c
Frequency Shifter 1
Frequency Shifter 2
Frequency Shifter N
Filter 1
Filter 2
Filter 3

FIG. 1

FOREIGN PATENT

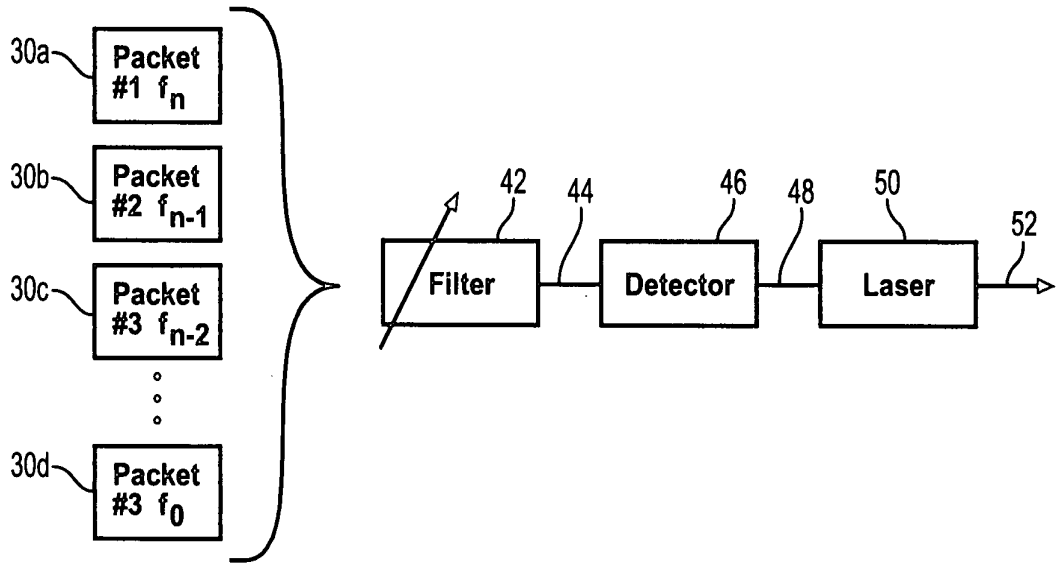


FIG. 2

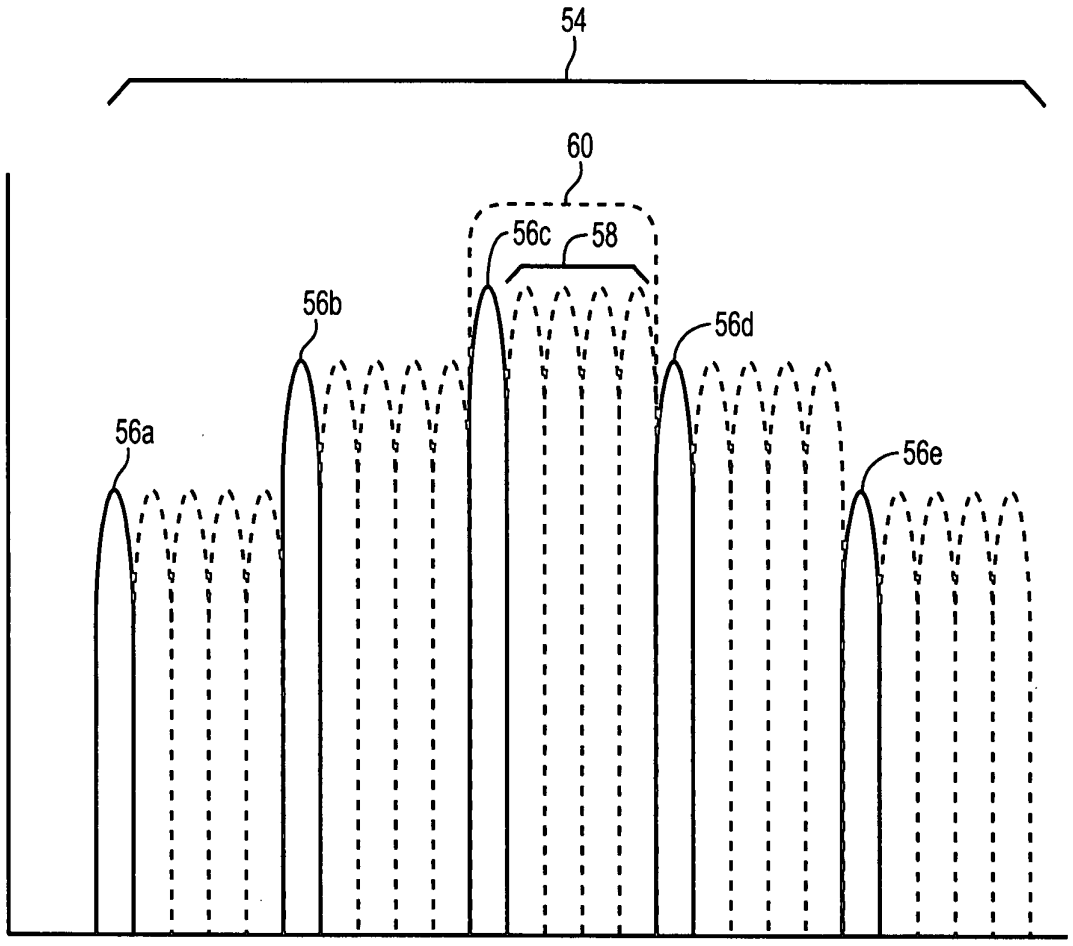


FIG. 3

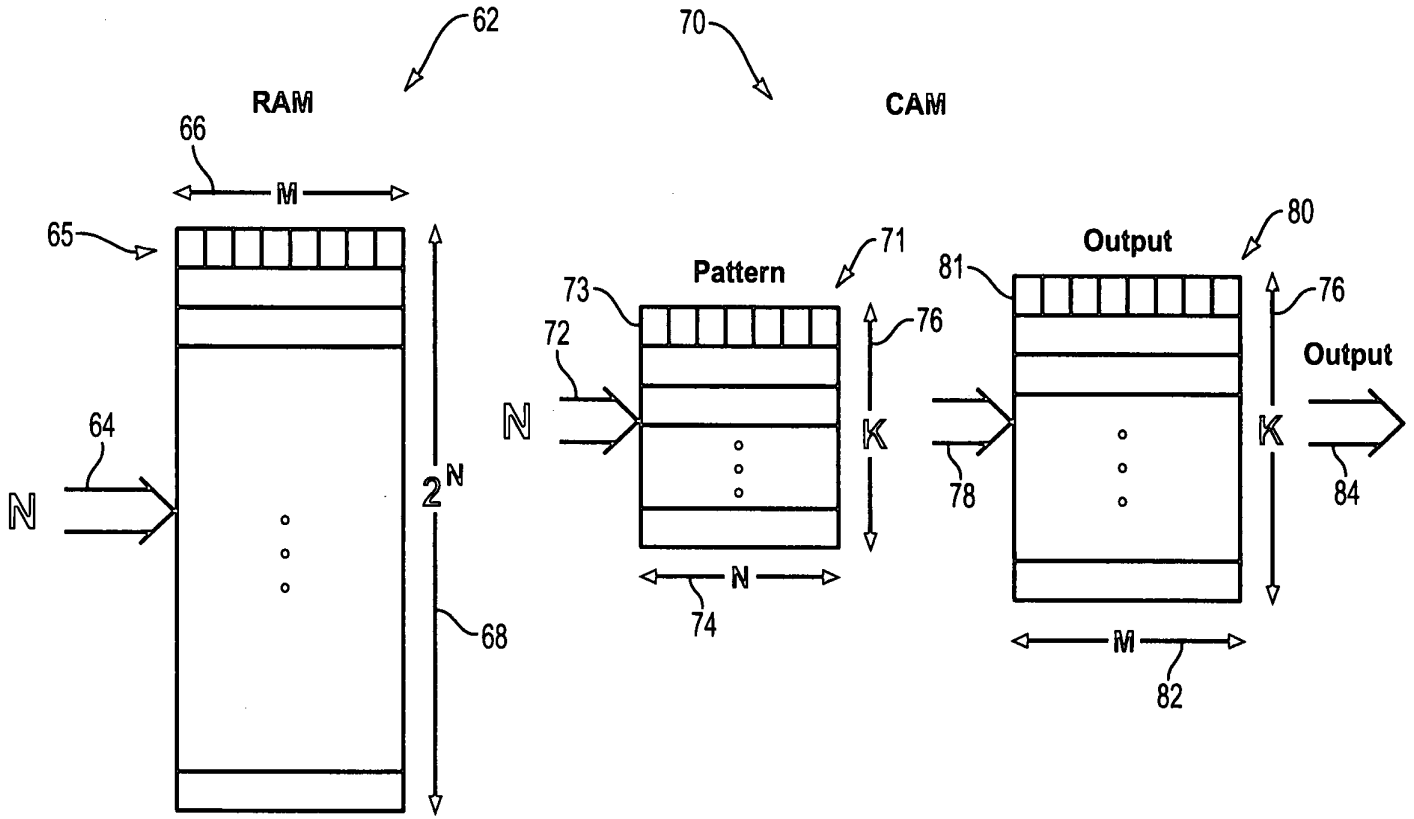

62
RAM
66
M
65
64
N
2^N
68
70
CAM
Pattern
71
73
72
N
K
76
74
Output
80
81
76
Output
K
78
84
M
82

FIG. 4

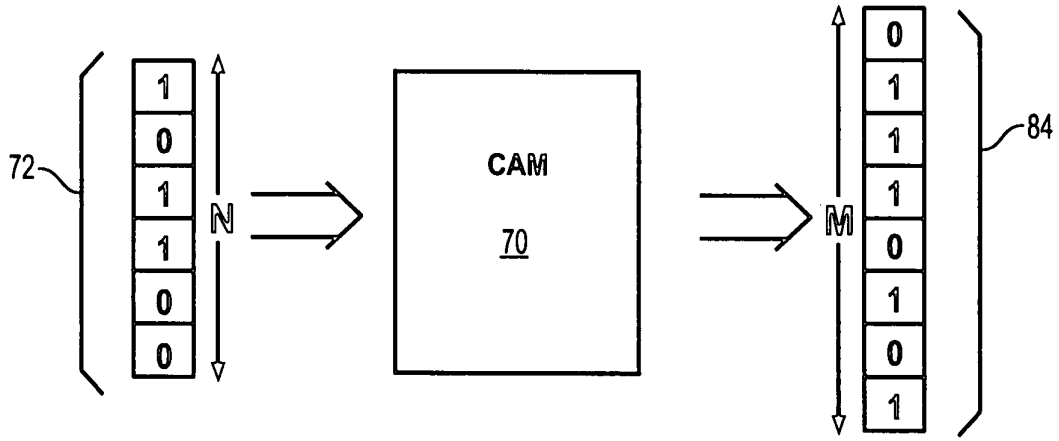


FIG. 5

PCT/SE2004/001860

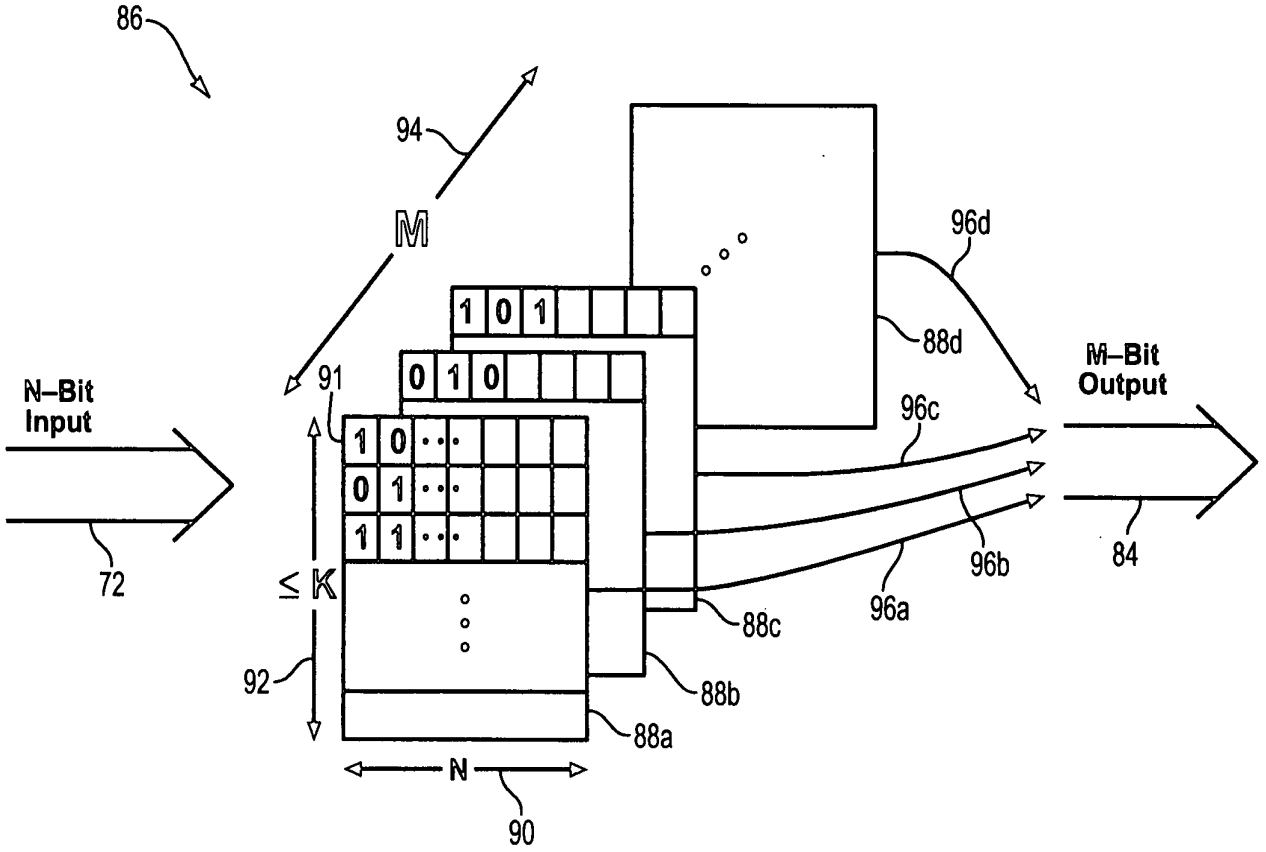
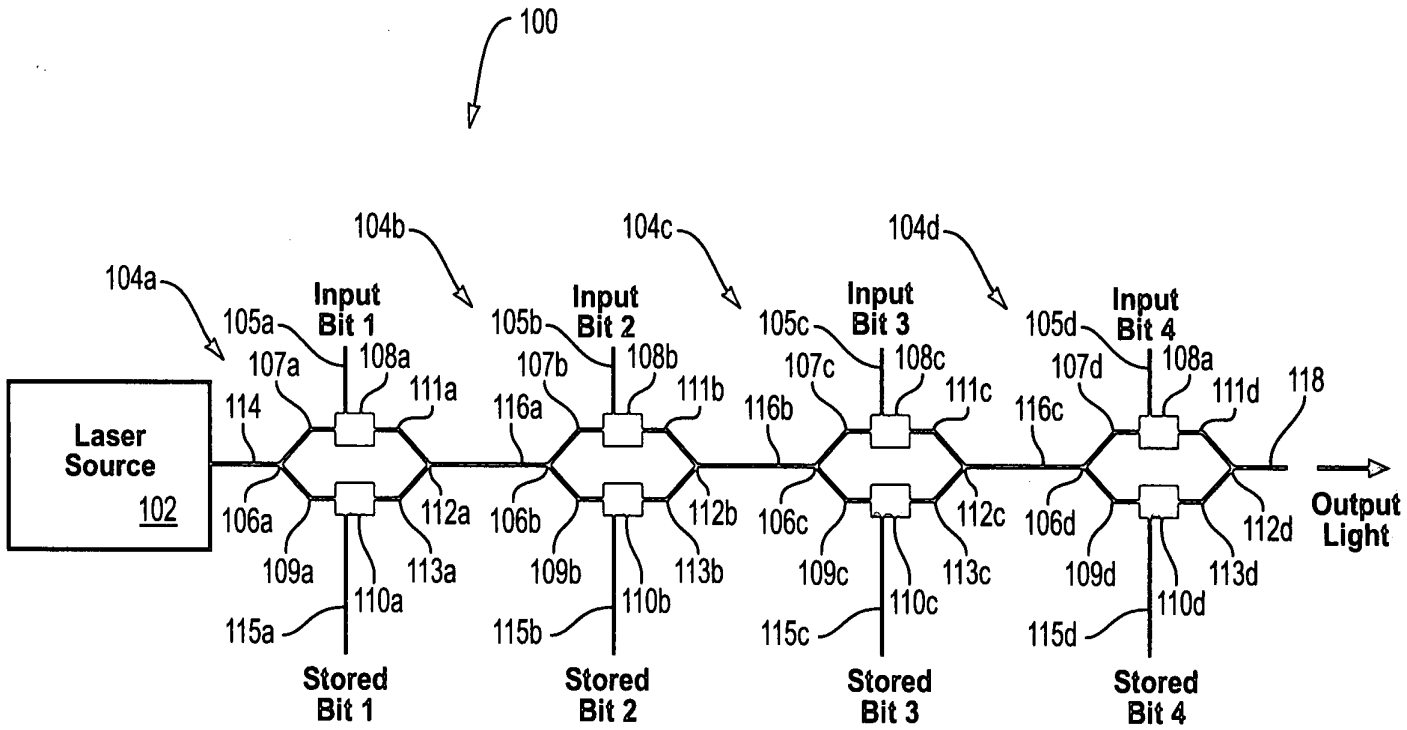


FIG. 6



Use of Mach – Zehnder Modulators
to Match Input Patterns

FIG. 7

Tuesday, February 08, 2005

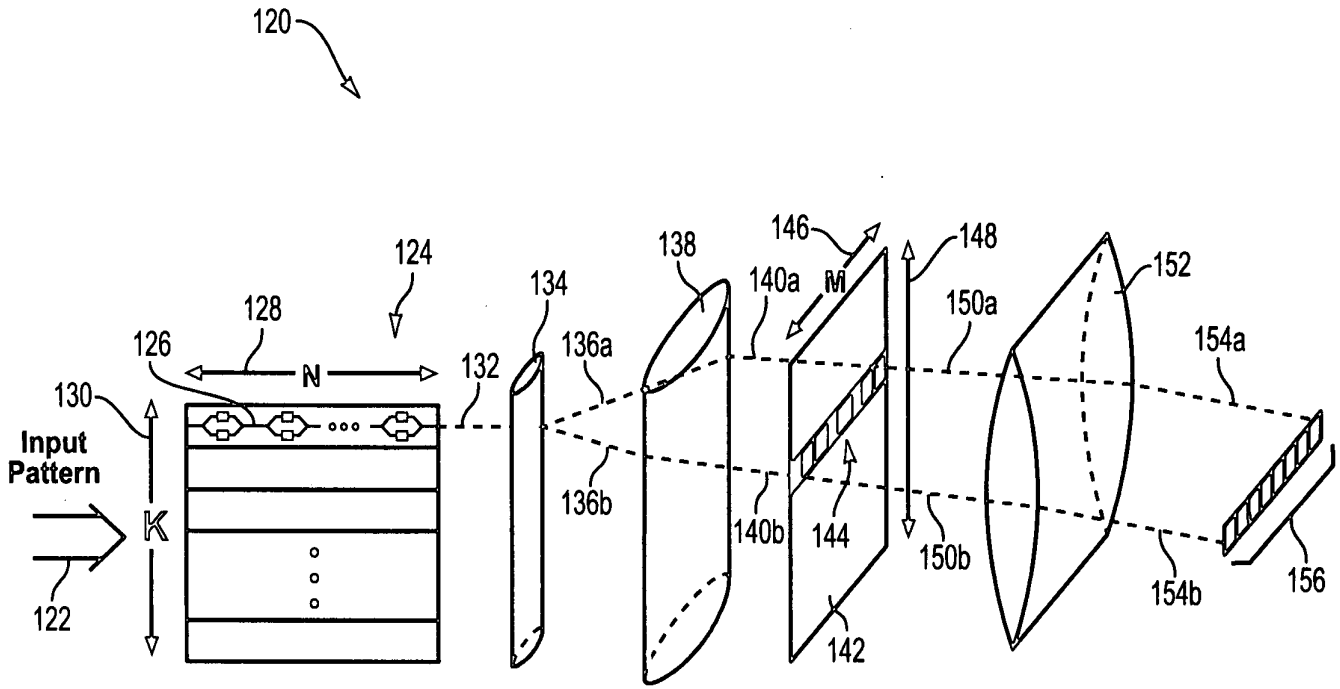


FIG. 8

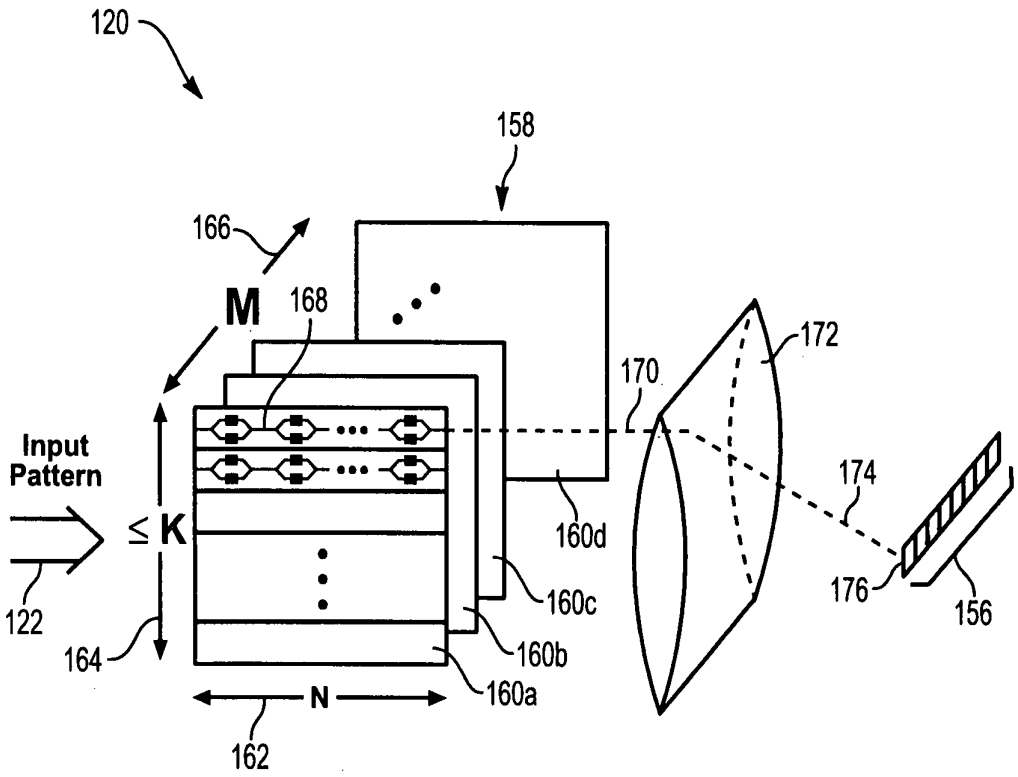

120
158
166
M
168
Input
Pattern
≤ K
122
164
N
162
160a
160b
160c
160d
170
172
174
176
156

FIG. 9

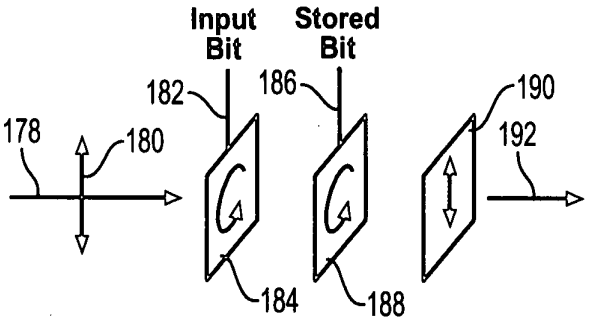


FIG. 10A

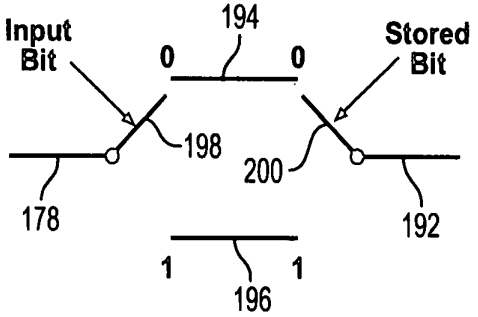


FIG. 10B

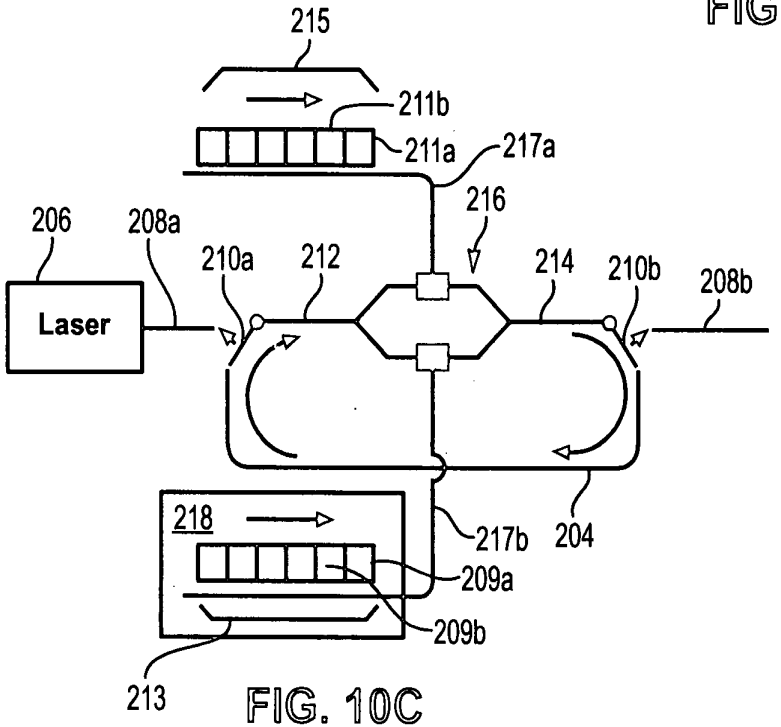


FIG. 10C

DRAFT - 2005-08-08

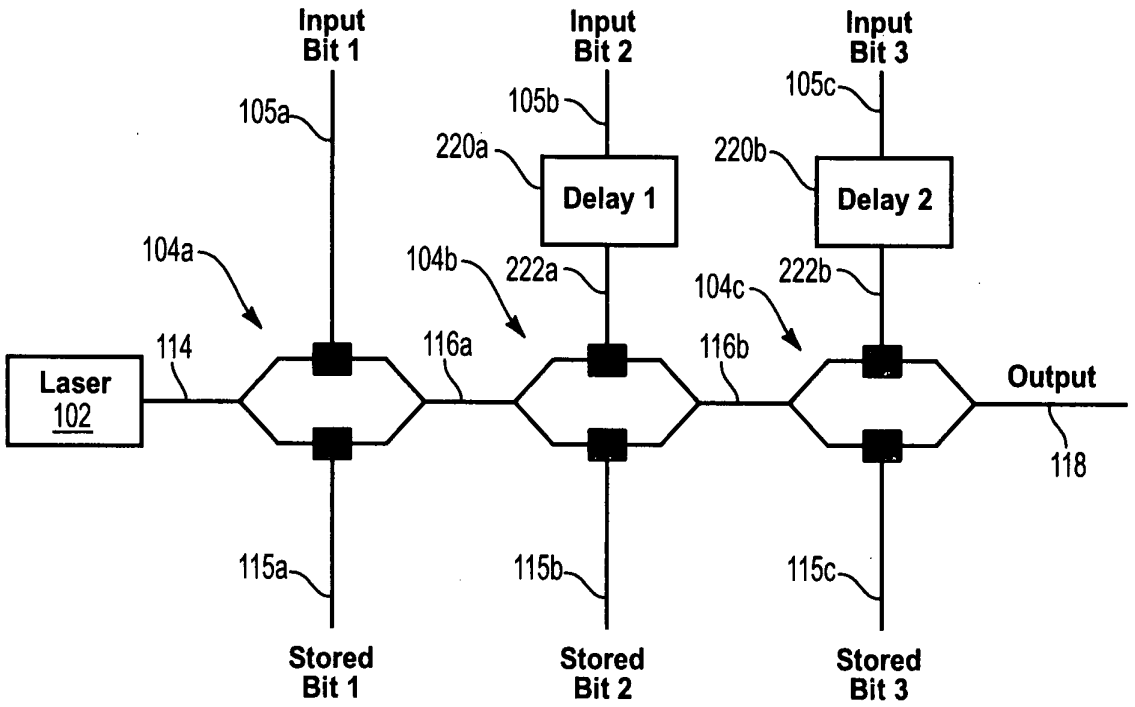


FIG. 11